TOSHIBA Field Effect Transistor Silicon N Channel MOS Type ( $L^2-\pi$ -MOSVI)

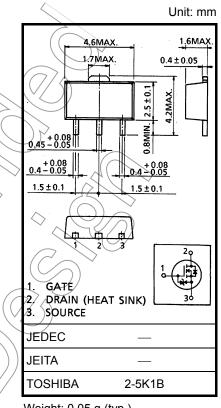
# 2SK2964

Chopper Regulators, DC-DC Converters and Motor DriveApplications

- 4-V gate drive
- Low drain-source ON-resistance: R<sub>DS</sub> (ON) = 0.13 Ω (typ.)
- High forward transfer admittance: |Y<sub>fs</sub>| = 2.5 S (typ.)
- Low leakage current: I<sub>DSS</sub> = 100 μA (max) (V<sub>DS</sub> = 30 V)
- Enhancement mode: V<sub>th</sub> = 0.8 to 2.0 V (V<sub>DS</sub> = 10 V, I<sub>D</sub> = 1 mA)

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V <sub>DSS</sub>	30	$\langle \mathbf{v} \rangle$
Drain-gate voltage (R <sub>GS</sub> = 20 kΩ)		V <sub>DGR</sub>	30	V
Gate-source voltage		V <sub>GSS</sub>	±20	v
Drain current	DC (Note 1)	l <sub>D</sub>	2	A
	Pulse (Note 1)	I <sub>DP</sub>	6	А
Drain power dissipation		P <sub>D</sub>	0.5	W
Drain power dissipation (Note 2)		PD	1.5	< w
Single pulse avalanche energy (Note 3)		EAS	56	mJ
Avalanche current			2 <	A
Repetitive avalanche energy (Note 4)		EAR	0.05	- mJ
Channel temperature		Tch	150	°C
Storage temperature range		Tstg	-55 to 150	∽°C



Weight: 0.05 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

## **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	250	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

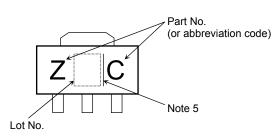
Note 2: Mounted on a ceramic substrate (25.4 mm × 25.4 mm × 0.8 mm)

Note 3:  $V_{DD}$  = 25 V,  $T_{ch}$  = 25°C (initial), L = 10 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 2 A

Note 4: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.

### Marking



Note 5: A line to the right of a Lot No. identifies the indication of product Labels. Without a line: [[Pb]]/INCLUDES > MCV With a line: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

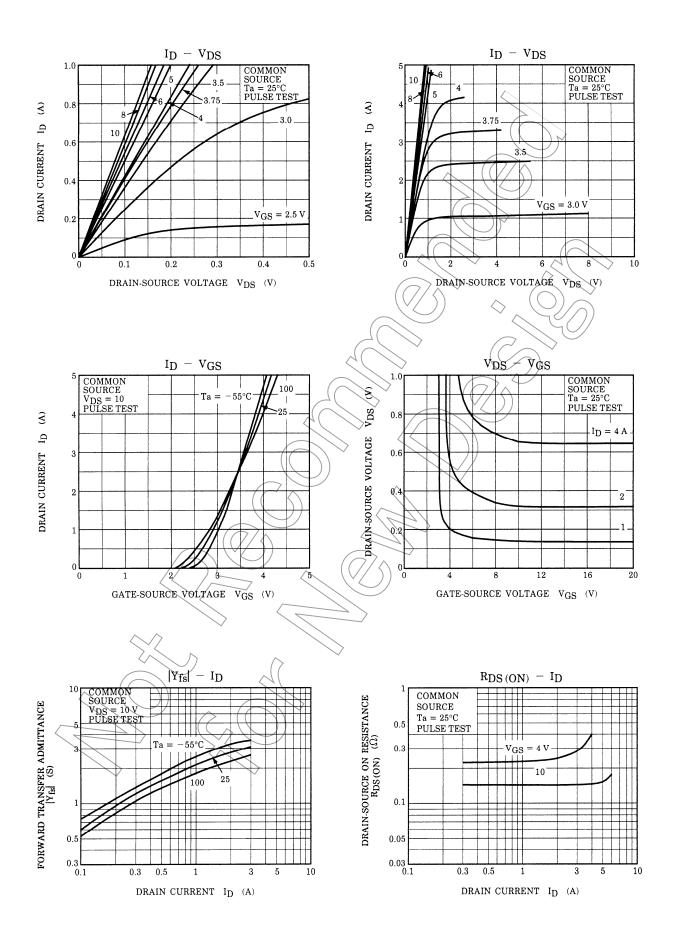
Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

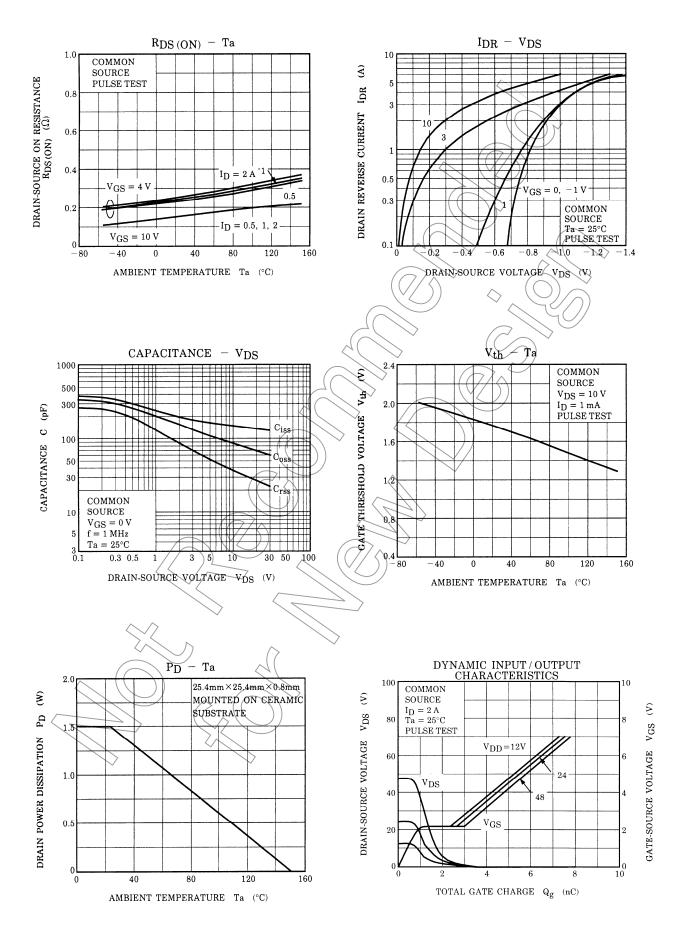
## Electrical Characteristics (Ta = 25°C)

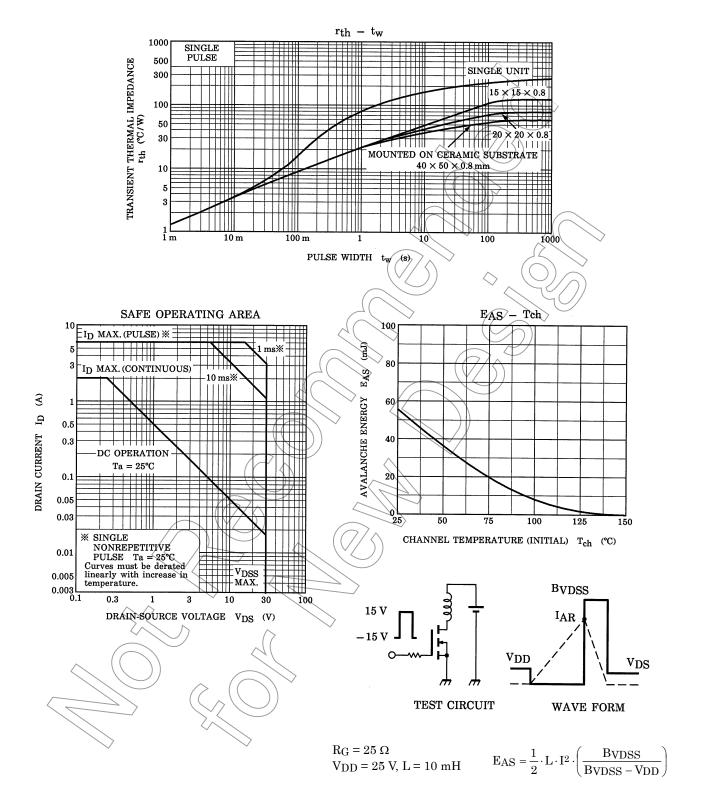
Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0 V		$\langle - \rangle$	> ±10	μA
Drain cut-off cu	rrent	I <sub>DSS</sub>	$V_{DS} = 30 V, V_{GS} = 0 V$	-((		100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	30	4 <i>(</i> )	/ _	V
Gate threshold v	voltage	V <sub>th</sub>	$V_{DS} = 10 V, I_D = 1 mA$	0.8	$\geq$	2.0	V
Drain-source ON-resistance		R <sub>DS (ON)</sub>	$V_{GS} = 4 V$ , $I_D = 1 A$	(A)	0.18	0.25	Ω
			V <sub>GS</sub> =10, V, I <sub>D</sub> =1 A		0.13	0.18	
Forward transfe	r admittance	Y <sub>fs</sub>	V <sub>D</sub> s=10 V, I <sub>D</sub> =1 A	) 1.2	2.5		S
Input capacitance	ce	C <sub>iss</sub>			140		
Reverse transfe	r capacitance	C <sub>rss</sub>	$V_{DS} = 10 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$		30		pF
Output capacita	nce	Coss			80		
Switching time	Rise time	(tr	$V_{GS} \stackrel{10}{_{0}} V \downarrow \downarrow$		10		
	Turn-on time	ton			15	_	ns
	Fall time	tr	0,7 m 0 d 0,7 m 0 d V <sub>DD</sub> ≈ 15 V Duty ≤ 1%, t <sub>w</sub> = 10 μs	_	85	_	
	Turn-off time	t <sub>off</sub>		_	195	_	
Total gate charg plus gate-drain)		Qg		_	5.8	_	
Gate-source ch	arge	Qgs	V <sub>DD</sub> ≈ 24 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 2 A	_	4.3	_	nC
Gate-drain ("mi	ller") Charge	Qgd		_	1.5	_	

## Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)		_		_	2	А
Pulse drain reverse current (Note 1)	IDRP	_		_	6	А
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 2 A, V <sub>GS</sub> = 0 V	_	_	-1.5	V
Reverse recovery time	t <sub>rr</sub>	- I <sub>DR</sub> = 2 A, V <sub>GS</sub> = 0 V, dI <sub>DR</sub> /dt = 50 A/μs	_	50	_	ns
Reverse recovery charge	Q <sub>rr</sub>	$1DR = 2 \Lambda$ , $VGS = 0 V$ , $UDR/Ut = 30 \Lambda/\mu s$	_	20	_	nC







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